

不用调试的数字分频器处理器 Digital frequency divider

数字声音处理器是恩平市影响电子科技有限公司工程师经过多年研发的心血之作,专门为CD、CO、LC、TB系列扬声器设计的精密分频电路,是CPL“君子龙”扬声器声音组合发挥能量的可靠性保证；每一个音箱的组合工程师经过长时间大量的检测调试，最终CPL“君子龙”数字声音处理器面世。CPL“君子龙”处理器大多数是3进6出设计，输出是厂家预设的参数是不可修改的，输入端具备现场调整功能，能够调整增益，延时，均衡，相位，压限现场需要修正的功能。

处理器3进6出的说明：
3IN6OUT 3IN=A(L),B(R),C(SUB) 6OUT=1(L-HF),2(L-LF),3(R-HF),4(R-LF),5(SUB),6(SUB)
输入通道A连接输出端的1和2，输出1连接左通道的高音，输出2连接左通道的低音。
输入通道B连接输出端的3和4，输出3连接右通道的高音，输出4连接右通道的低音。
输入通道C连接输出端的5和6，输出5连接超低音箱，输出6连接超低音箱。

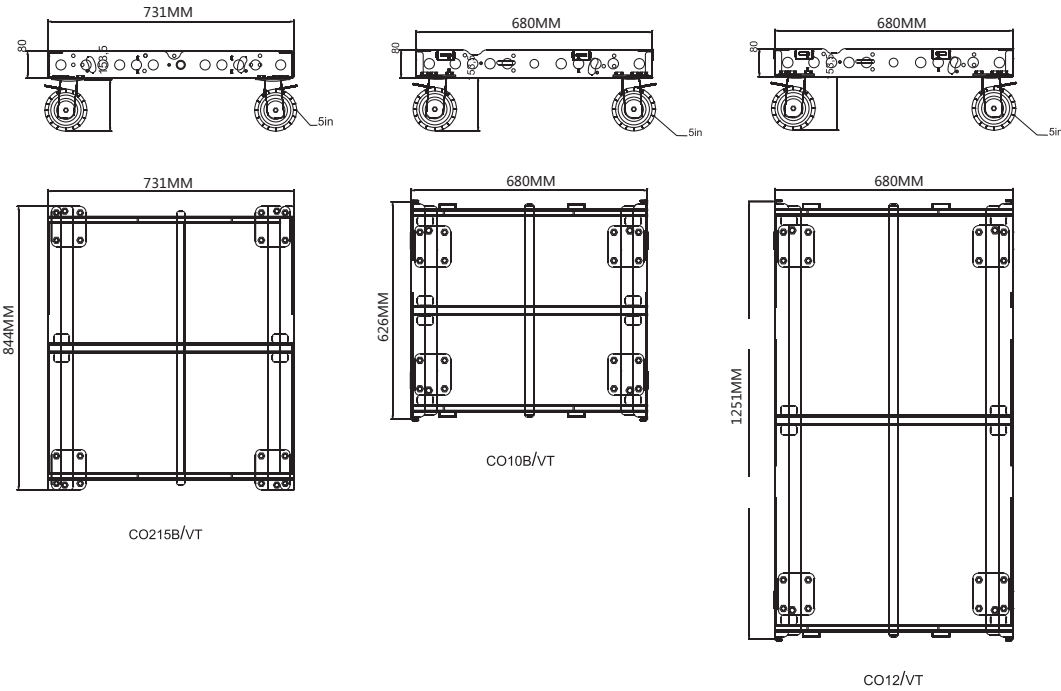
CO系列处理器里面CO8system和CO10system操作方法和LC系列一样，都是3进6出设计，其中CO12是3分频设计所以CO12system是4进8出设计的电路。

4IN8OUT 4IN= A(L),B(R),C(SUB),D(SUB)
8OUT=1(L-HF),2(L-MF),3(L-LF),4(R-HF),5(R-MF),6(R-LF),7(SUB),8(SUB)
输入通道A连接输出端的1，2，3输出1连接左通道的高音，输出2连接左通道的中音，输出3连接左通道的低音。
输入通道B连接输出端的4，5，6输出4连接右通道的高音，输出5连接右通道的中音，输出6连接右通道的低音。
输入通道C连接输出端的7，输出7连接超低音箱。
输入通道D连接输出端的8，输出8连接超低音箱。

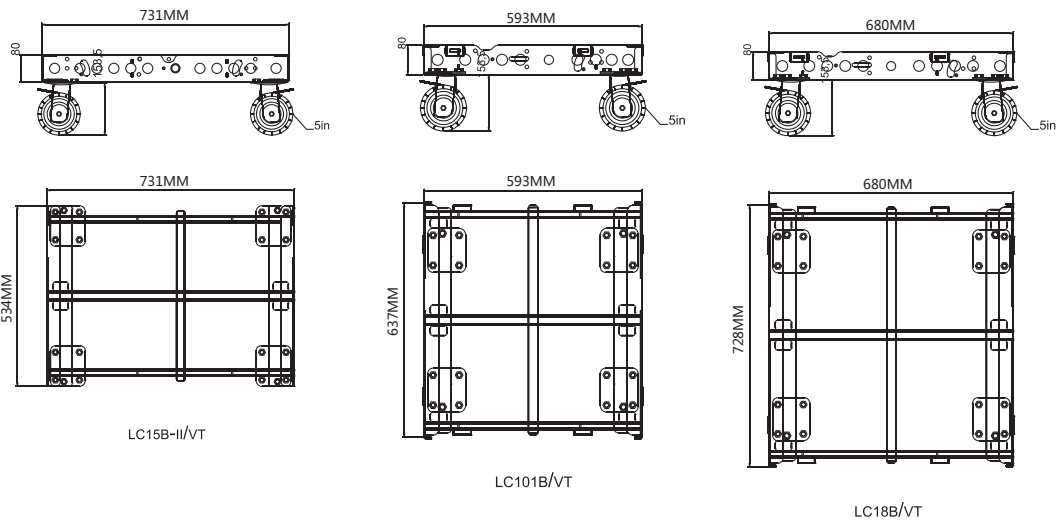
Processor Description:
LC and CO Series Digital Sound Processor developed by Affected Audio Technology CO. engineers after many years of hard work to make specifically for LC and CO Series speaker design precision divider circuit is LC and CO Series loudspeakers play sound energy portfolio Reliability Assurance; each speaker combination to a large number of engineers after a long testing and commissioning, final LC and CO series digital sound processor available.
LCand CO processors are mostly 3 into 6 design, the output is factory preset parameters can not be modified, with the input of the field adjustment, can adjust the gain, delay, equalization, phase, pressure limit, the field needs correction function.

Processor 3 into six instructions:
3IN6OUT 3IN = A (L), B (R), C (SUB) 6OUT = 1 (L-HF), 2 (L-LF), 3 (R-HF), 4 (R-LF), 5 (SUB), 6 (SUB)
A connecting the output of the input channels 1 and 2, the output of a connection left channel treble, bass output 2 connecting the left channel.
Channel B input connected to an output terminal 3 and 4, the output connected to the right channel treble 3, 4 connecting the right channel output bass.
Input channels to connect the output of the C 5 and 6, 5 are connected subwoofer output, connect the output of six subwoofers.

CO10system series processors inside CO8system and methods of operation and LC series, all three into six designs, which is a three-way design CO12 so CO12system four into eight design of the circuit.
4IN8OUT
4IN = A (L), B (R), C (SUB), D (SUB) 8OUT = 1 (L-HF), 2 (L-MF), 3 (L-LF), 4 (R-HF), 5 (R-MF), 6 (R-LF), 7 (SUB), 8 (SUB)
A connecting the output of the input channels 1,2,3 connect the left channel output a treble, alto 2 Connect the left channel output, the output 3 Connect the left channel bass.
Channel B input connected to an output terminal connected to the right channel output 4 4,5,6 treble, midrange output connected to the right channel 5, 6 connecting the right channel output bass.
C input channels to connect the output of 7, 7 connected subwoofer output.
D input channels to connect the output of 8, 8 connected subwoofer output.



CO215B /VT	能单独吊装或组合吊装CO8和CO215B	带四个脚轮时能当CO8和CO215B的推车
CO10B /VT	能单独吊装或组合吊装CO10和CO10B	带四个脚轮时能当CO10和CO10B的推车
CO12 /VT	能单独吊装CO12	带四个脚轮时能当CO12的推车



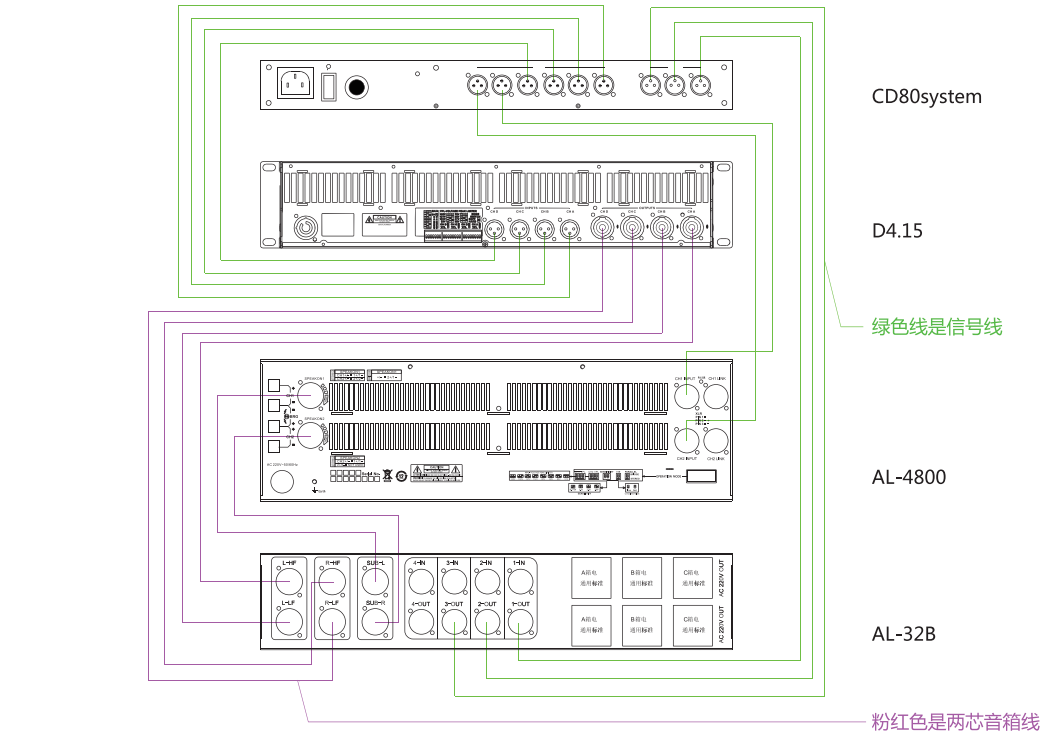
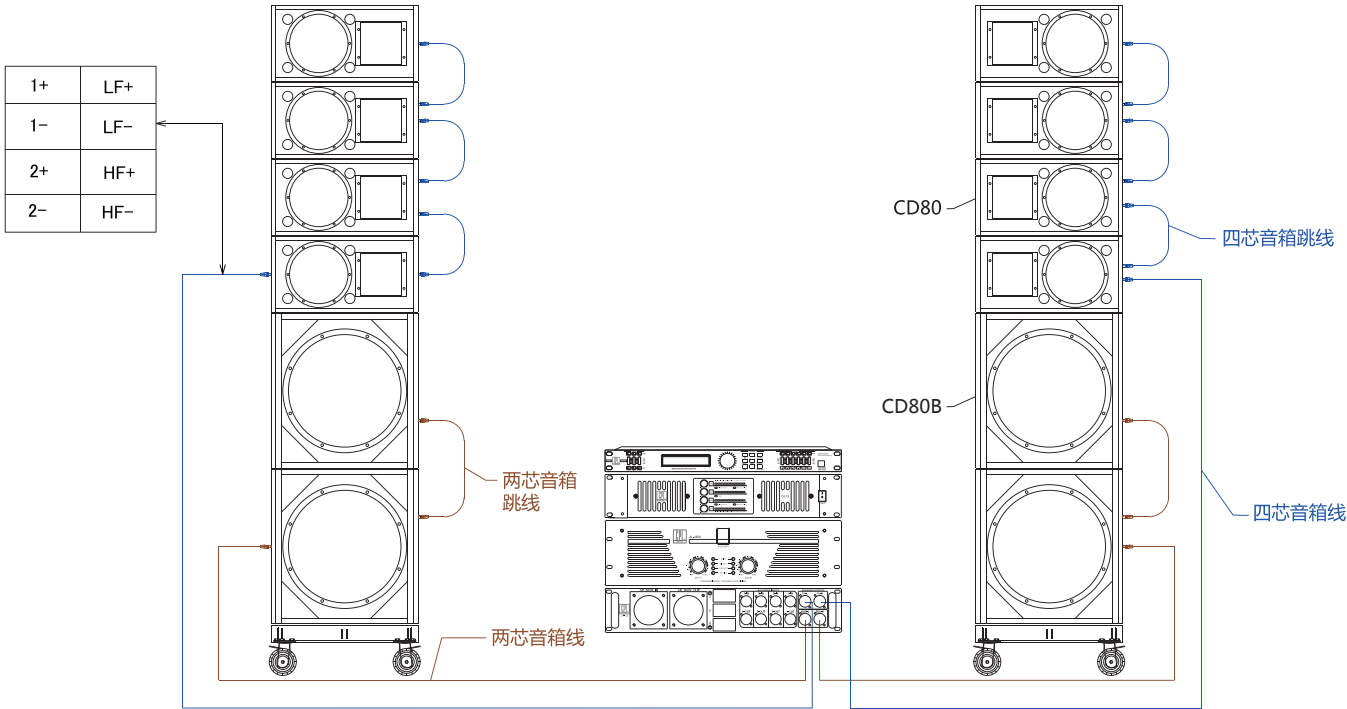
LC18B/VT	能单独吊装LC110,LC18B,LC200,LC150,LC120,LC215B,LC80	带四个脚轮时能当LC110,LC120,LC150,LC18B,LC200,LC215B,的推车
LC101B/VT	能单独吊装或组合吊装LC101,LC101B	带四个脚轮时能当LC101,LC101B的推车
LC15B-II/VT	能单独吊装或组合吊装LC15B-II,LC81	带四个脚轮时能当LC150B-II,LC81的推车
CD120B/VT	能单独吊装或组合吊装CD120,CD120B	带四个脚轮时能当CD120,CD120B的推车
CD100B/VT	能单独吊装或组合吊装CD100,CD100B	带四个脚轮时能当CD100,CD100B的推车
CD80B/VT	能单独吊装或组合吊装CD80,CD80B	带四个脚轮时能当CD80,CD80B的推车

CD80+CD80B(4+2B)

- D4.15功放输入灵敏度：150V (35dB)
- AL - 4800功放输入灵敏度：0.775V (180V)
- 处理器数据选择：4+2B

CD80system processor(IN)：A=L B=R C=SUB

CD80system processor(OUT)：1=L - HF 2=L - LF 3=R - HF 4=R - LF 5=SUB 6=SUB

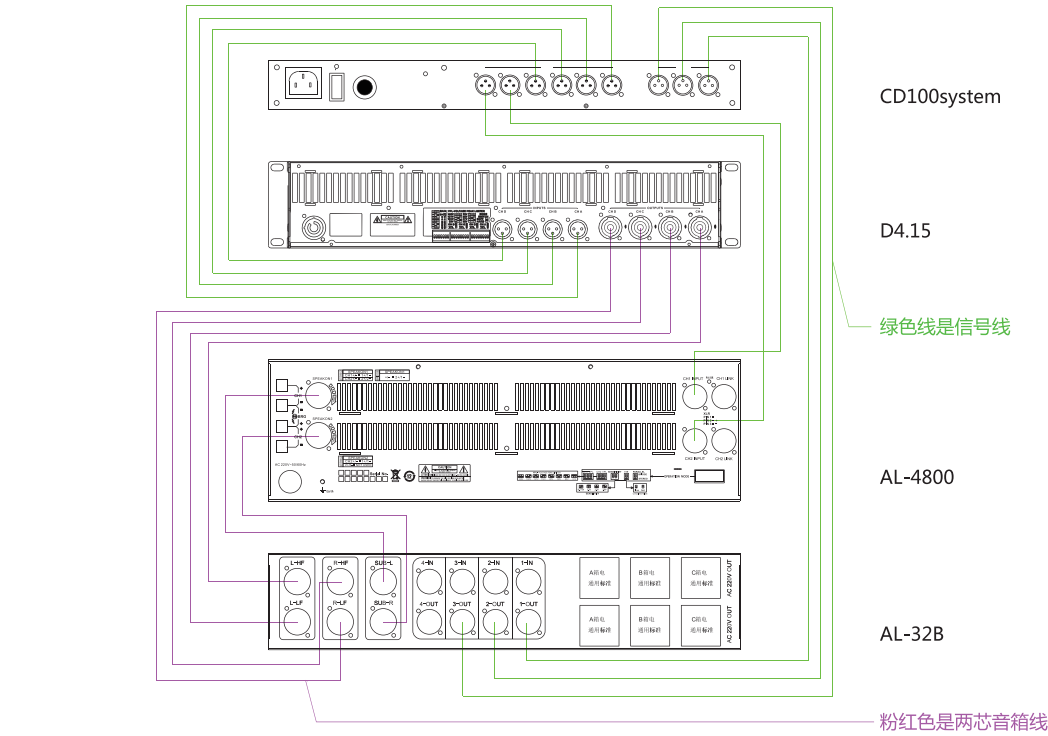
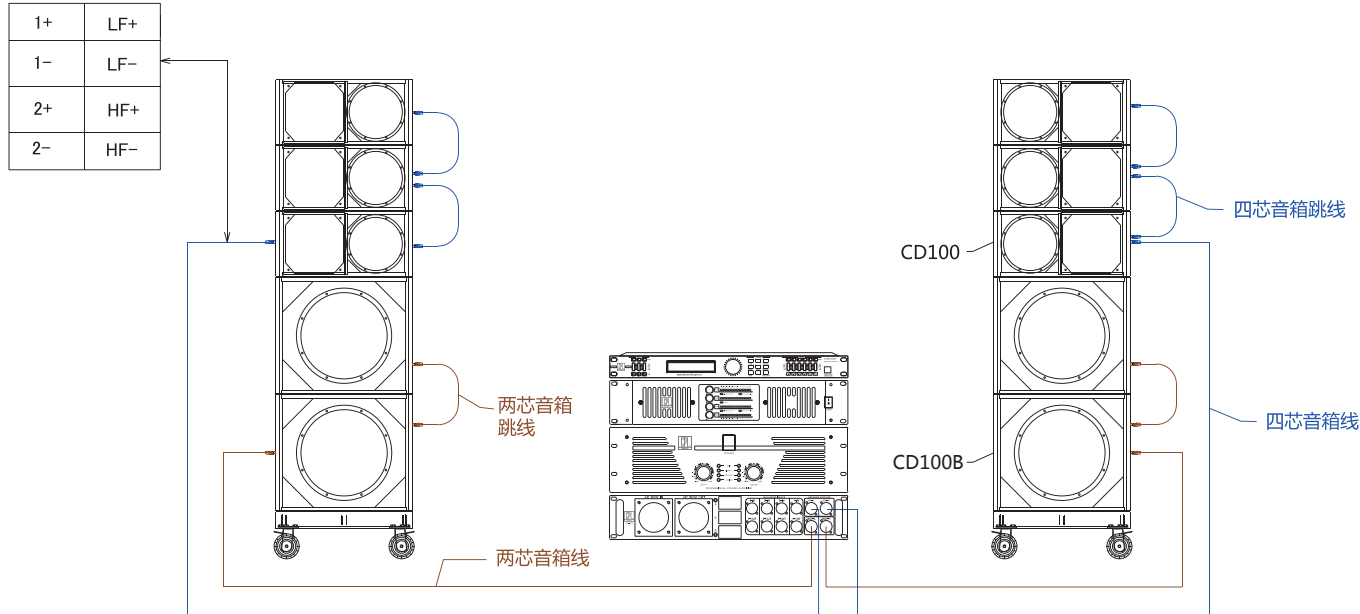


CD100+CD100B(3+2B)

- D4.15功放输入灵敏度：150V (35dB)
- AL - 4800功放输入灵敏度：0.775V (180V)
- 处理器数据选择：3+2B

CD100 system processor(IN)：A=L B=R C=SUB

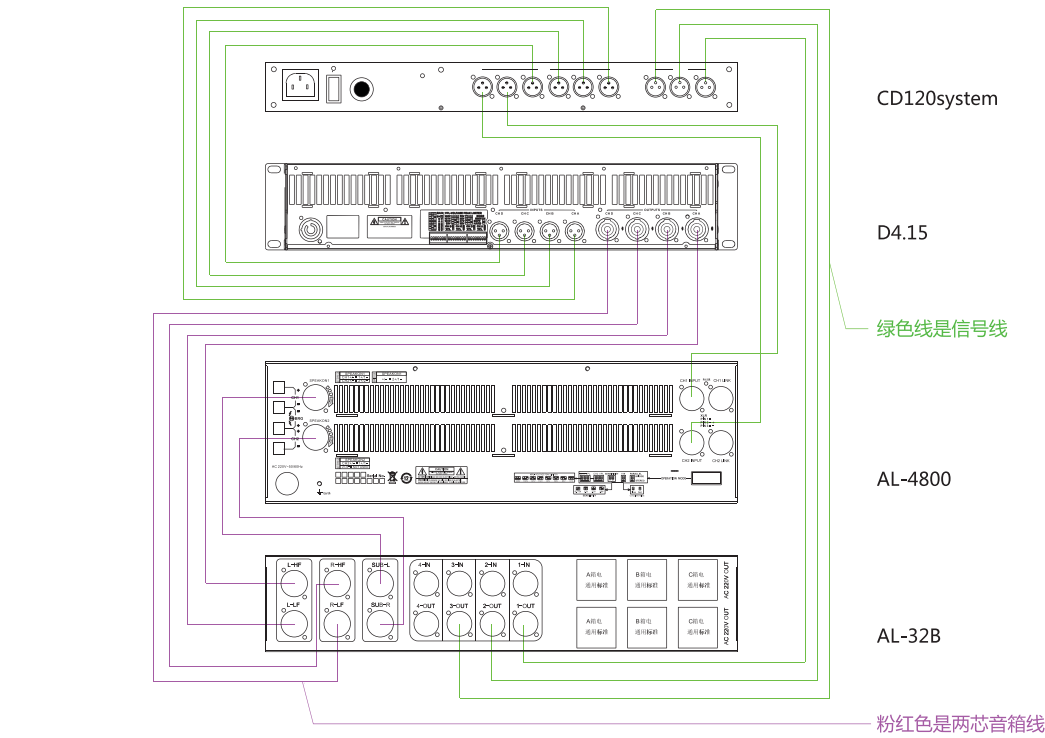
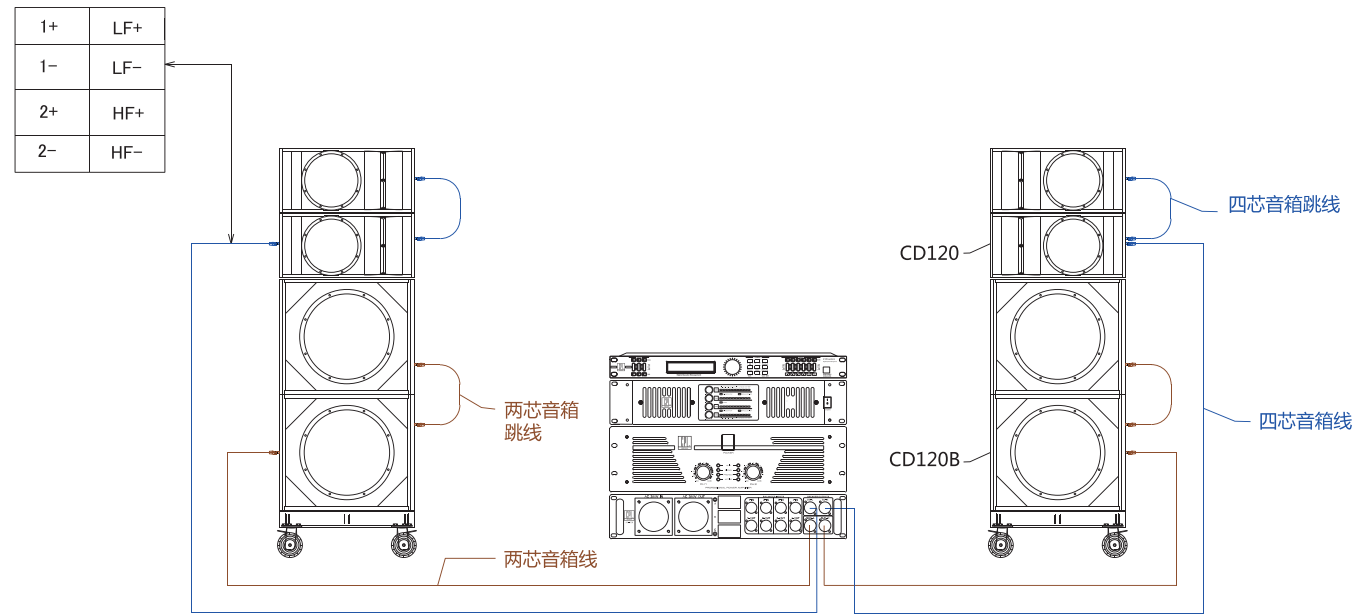
CD100 system processor(OUT)：1=L - HF 2=L - LF 3=R - HF 4=R - LF 5=SUB 6=SUB



CD120+CD120B(2+2B)

- 1、D4.15功放输入灵敏度：150V (35dB)
- 2、AL - 4800功放输入灵敏度：0.775V (180V)
- 3、处理器数据选择：2+2B

CD120 system processor(IN)：A=L B=R C=SUB
CD120 system processor(OUT)：1=L - HF 2=L - LF 3=R - HF 4=R - LF 5=SUB 6=SUB



CO8-II+CO215B(3+2B)

- 1、D4.15功放输入灵敏度：150V (35dB)
- 2、AL - 4800功放输入灵敏度：0.775V (180V)
- 3、处理器数据选择：2+2B

CO8 system processor(IN)：A=L B=R C=SUB
CO8 system processor(OUT)：1=L - HF 2=L - LF 3=R - HF 4=R - LF 5=SUB 6=SUB

